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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:
Allen, et al.

Serial No.: 10/709,292

Filed: April 27, 2004

Group Art Unit: 2825

Examiner: Lam, Nelson C.

Atty. Docket No.: BUR920030135US1

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Pamela M. Riley

For: INTEGRATED CIRCUIT YIELD ENHANCEMENT USING VORONOI DIAGRAMS

AMENDMENT UNDER 37 C.F.R. §1.111

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This amendment is in response to the Office Action mailed September 26, 2006, setting a three-month statutory period for response. Therefore, this amendment is timely filed. Please amend the above-identified patent application as follows:

10/709,292

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